

# Single P-channel MOSFET

## ELM34537BA-N

<http://www.elm-tech.com>

### ■ General description

ELM34537BA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and low gate resistance.

### ■ Features

- $V_{ds} = -30V$
- $I_d = -11A$
- $R_{ds(on)} < 9m\Omega$  ( $V_{gs} = -10V$ )
- $R_{ds(on)} < 14m\Omega$  ( $V_{gs} = -4.5V$ )

### ■ Maximum absolute ratings

$T_a = 25^\circ C$ . Unless otherwise noted.

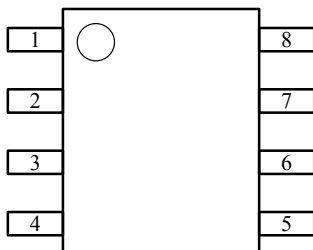
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	$V_{ds}$	-30	V	
Gate-source voltage	$V_{gs}$	$\pm 25$	V	
Continuous drain current	$I_d$	$-11.0$	A	
		$-8.7$		
Pulsed drain current	$I_{dm}$	-50	A	3
Avalanche current	$I_{as}$	-35	A	
Avalanche energy	$E_{as}$	61	mJ	
Power dissipation	$P_d$	1.8	W	
		1.2		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	$^\circ C$	

### ■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$		68	$^\circ C/W$	4
Maximum junction-to-case	$R_{\theta jc}$		25		

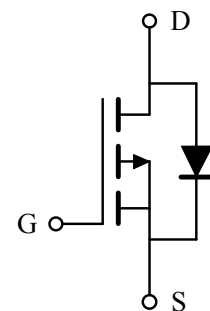
### ■ Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

### ■ Circuit



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### ■Electrical characteristics

Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
<b>STATIC PARAMETERS</b>							
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-30			V	
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V			-1	μA	
		Vds=-20V, Vgs=0V, Ta=55°C			-10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±25V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-1.0	-1.6	-3.0	V	
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-11A		7.2	9.0	mΩ	1
		Vgs=-4.5V, Id=-11A		10.4	14.0		
Forward transconductance	Gfs	Vds=-10V, Id=-11A		40		S	1
Diode forward voltage	Vsd	If=-11A, Vgs=0V			-1.3	V	1
Max. body-diode continuous current	Is				-11	A	
<b>DYNAMIC PARAMETERS</b>							
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz		2664		pF	
Output capacitance	Coss			374		pF	
Reverse transfer capacitance	Crss			271		pF	
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		3.7		Ω	
<b>SWITCHING PARAMETERS</b>							
Total gate charge (Vgs=-10V)	Qg	Vds=-15V, Id=-11A		56		nC	2
Total gate charge (Vgs=-4.5V)	Qg			28		nC	2
Gate-source charge	Qgs			9		nC	2
Gate-drain charge	Qgd			13		nC	2
Turn-on delay time	td(on)	Vgs=-10V, Vds=-15V		22		ns	2
Turn-on rise time	tr			26		ns	2
Turn-off delay time	td(off)		Id=-11A, Rgen=6Ω		102		ns
Turn-off fall time	tf			75		ns	2
Reverse recovery time	trr	If=-11A, dIf/dt=100A/μs		26		ns	
Reverse recovery charge	Qrr				14		μC

#### NOTE :

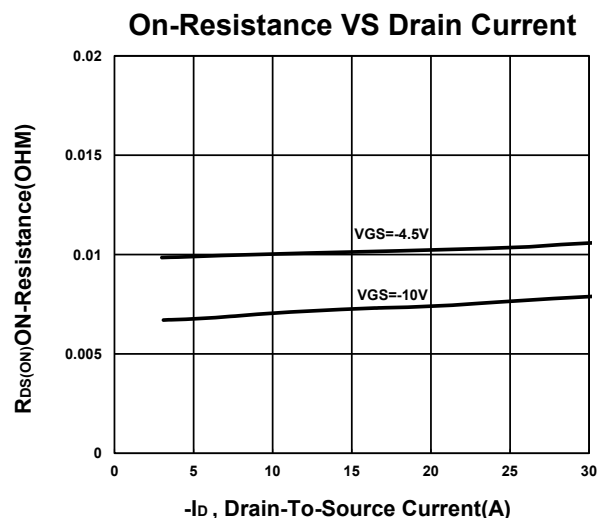
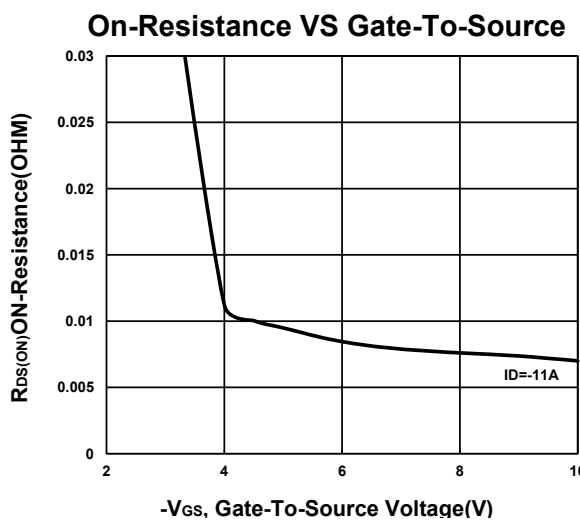
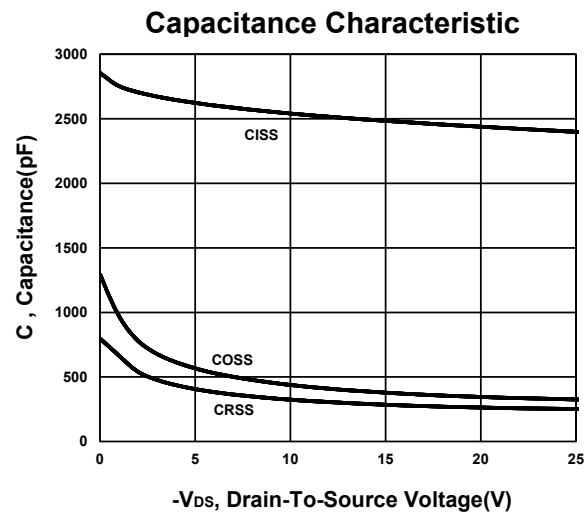
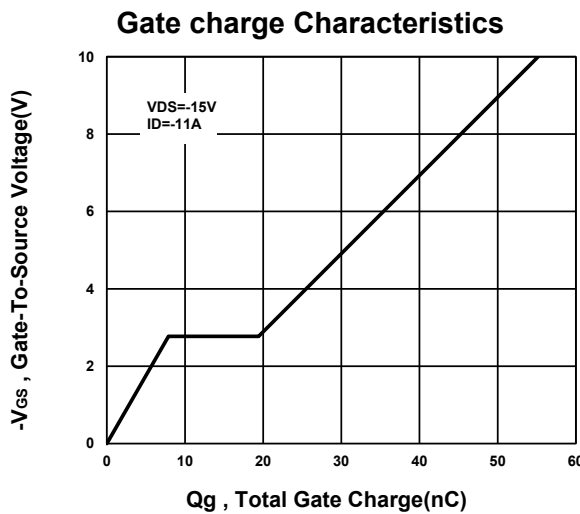
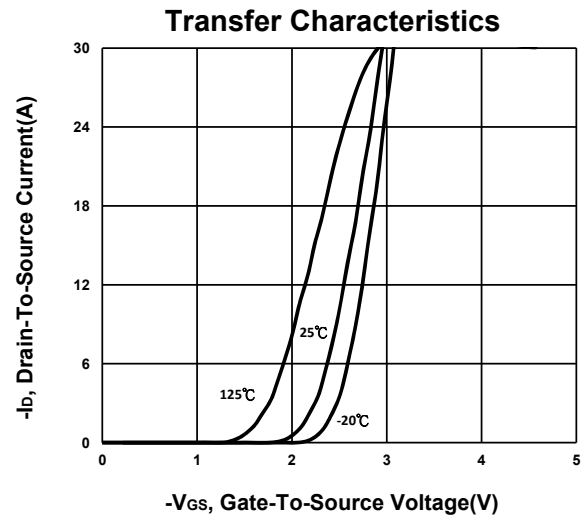
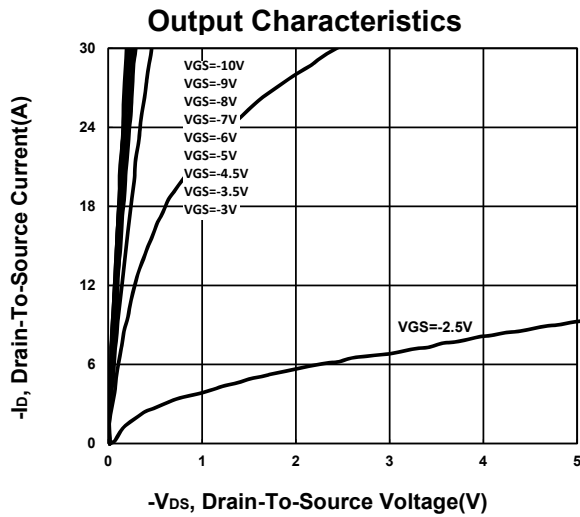
1. Pulsed test : Pulsed width≤300μsec and Duty cycle≤2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. The value of Rθja is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with Ta =25°C.

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## Typical electrical and thermal characteristics



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